


Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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EFS ID:	69099	
Application ID:	10711532	
Title of Invention:	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages	
First Named Inventor:	Bhupendra SHARMA	
Domestic/Foreign Application:	Domestic Application	
Filing Date:	2004-09-24	
Effective Receipt Date:	2004-09-24	
Submission Type:	Utility Patent Filing	
Filing Type:	new-utility	
Confirmation number:	5531	
Attorney Docket Number:	TI-38242	
Total Fees Authorized:	770.0	
Payment Category:	Deposit Account	
Deposit Account Number:	200668	
Deposit Account Name:	Narendra R. Thappeta	
RAM Payment Status:	RAM has not been processed	

Digital Certificate Holder: cn=Narendra R. Thappeta,ou=Registered Attorneys,ou=Patent and Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: cd30f730571cdb60f5485e755510a236ed80fc3c

PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

09/24/2004 HTECKLU1 00000025 200668 10711532

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PTO-1556
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